<u>Amendments to the Claims:</u> This listing of claims will replace all prior versions, and listings, of claims in the application

Listing of Claims:

1. (Original) A method of driving a plasma display panel, the panel comprising:

a plurality of display electrode pairs each one of the display electrode pairs including a scan electrode and a sustain electrode disposed on a first substrate;

a plurality of priming electrodes disposed in parallel with and between every other display electrode pairs disposed on the first substrate; and

a plurality of data electrodes disposed on a second substrate, confronting the first substrate with a discharge space therebetween, such that the data electrodes are placed in a direction crossing the display electrode pairs,

wherein the display electrode pairs confront the data electrodes for forming primary discharge cells, and the priming electrodes confront the data electrodes for forming priming discharge cells,

wherein one field comprises a plurality of sub-fields each one of the sub-fields including an initializing period, an addressing period, and a sustaining period,

wherein the addressing period includes an odd-line addressing period in which an address operation is conducted to primary discharge cells having odd-number scan electrodes, an even-line addressing period in which an address operation is conducted to primary discharge cells having even-number scan electrodes,

wherein during the odd-line addressing period, a scan pulse voltage is applied to oddnumber scan electrodes sequentially while a priming pulse voltage is applied, prior to the application of the scan pulse voltage, to a priming electrode adjacent to the scan electrode to which the scan pulse voltage is to be applied, in order to generate a priming discharge between the priming electrodes and the data electrodes, and wherein during the even-line addressing period, a scan pulse voltage is applied to evennumber scan electrodes sequentially while a priming pulse voltage is applied, prior to the application of the scan pulse voltage, to a priming electrode adjacent to the scan electrode to which the scan pulse voltage is to be applied, in order to generate a priming discharge between the priming electrodes and the data electrodes.

- 2. (Original) The method of claim 1, wherein during the addressing period, a time span of applying the scan pulse voltage to the scan electrodes overlaps a time span of applying the priming pulse voltage to the priming electrode.
- 3. (Currently Amended) The method of claim 1—or claim 2, wherein an auxiliary initializing period is provided between the odd-line addressing period and the even-line addressing period for conducting an initializing discharge between the priming electrode and the data electrode.

LEA/ds

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I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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